Self-standing chitosan films as dielectrics in organic thin-film transistors

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Abstract. Organic thin film transistors, using self-standing 50 mm thick chitosan films as dielectric, are fabricated using sublimed pentacene or two conjugated polymers deposited by spin coating as semiconductors. Field-effect mobilities are found to be similar to values obtained with other dielectrics and, in the case of pentacene, a value (0.13 cm²/(V·s)) comparable to high performing transistors was determined. In spite of the low On/Off ratios (a maximum value of 600 was obtained for the pentacene-based transistors), these are promising results for the area of sustainable organic electronics in general and for biocompatible electronics in particular.

Keywords: biocompatible polymers, organic thin-film transistors, organic field-effect transistors, chitosan, dielectric

1. Introduction

There is a significant effort being put into the development of organic electronics employing various renewable materials, encompassing paper, leather, silk and biodegradable plastics, for various functions [1]. There are very interesting possibilities in terms of the development of ‘green’, biocompatible, and biodegradable electronics with low production costs. For instance, various researches are being focused on paper. This has been studied as a substrate for printed circuits, using low cost fabrication methods. The fact that these systems combine flexibility and lightweight opens up various areas of application such as consumer electronics, disposable systems and applications in medical sensing and micro-electromechanical systems [2]. Paper was also used as material for flexible electronics [3]. Chitosan (CS) is a cationic polysaccharide obtained by alkaline deacetylation of chitin, this being the second most abundant polymer in nature, after cellulose. Chitin appears in nature as ordered crystalline microfibrils forming structural components of the exoskeleton of crustacean and insects and in the cell walls of fungi and algae [4, 5]. In view of its properties, like excellent film-forming ability, biocompatibility and biodegradability [4] chitosan and derivatives have been used in biomedical applications, such as gene delivery, biosensors [6] and also as proton exchange membranes [7]. These applications offer the prospects for the fabrication of biocompatible devices that could operate in biologically relevant media (for instance for smart sensing and preventive medical care), an area that is attracting an increasing attention [8].

In 2011, two reports appeared on the use of chitosan-based materials in electronics. One paper, by Zhong et al. [9], explored the protonic conduction in maleic-chitosan thin films to fabricate a protonic...
field-effect transistor, yielding a proton mobility of 4.9-10^{-3} \text{cm}^2/(\text{V} \cdot \text{s}). In another study, chitosan was used as dielectric in an ITO-based thin film transistor [10]. In this study, a chitosan film was deposited on ITO-coated glass by drop cast from an acetic acid solution. No indication of its thickness was given. ITO source, drain and channel were fabricated by RF magnetron sputtering. The devices were tested at ambient conditions. The authors obtained high performance transistors, which they attribute to the influence of proton mobility leading to very high gate dielectric capacitance due to electric-double-layer effect at the interface between dielectric and the semiconducting oxide. No reports were found on the use of CS as dielectric material in organic transistors. In view of the CS potential, we have been studying self-standing CS films both as substrates and dielectrics for organic thin film transistors (TFTs). Here, we report on the performance of TFTs fabricated with organic semiconductors deposited by sublimation (pentacene) and by spin coating (regioregular head-to-tail poly(3-hexylthiophene), P3HT, and poly(9,9-dioctylfluorene-alt-bithiophene), F8T2), on the bare surface of self-standing CS films. We find high hole field-effect mobilities (peaking at 0.13 cm^2/(V·s) for pentacene) though the On/Off ratios are small. A strong hysteresis effect is found which is attributed to the presence of ionic mobile species in the CS films.

## 2. Experimental

Chitosan, with a degree of deacetylation of 97% and a viscosity-average molar mass of 350 000 g/mol, was purchased from Mahtani Chitosan Pvt. Ltd. (India) and purified before use [11]. For the preparation of the CS films, a solution 1.5% (w/v) at pH = 4 was first prepared by dissolving the purified powdered CS sample in aqueous acetic acid (1% v/v) at room temperature during 48 h. An appropriate mass of CS solution was then transferred onto 10 × 10 cm^2 square plates. ~50 µm thick films were obtained by casting at 30°C in a ventilated oven for 16 h. The obtained films were kept in atmospheric conditions until they were used for the thin film transistors fabrication. The surface of the CS films were characterized by atomic force microscopy, using a Molecular Imaging Agilent (model 5100) system, operating in non-contact mode (at a resonance frequency between 200 and 400 kHz) using silicon probes with tip radii lower than 10 nm.

Thin-film transistors were prepared by depositing, first, a gate contact of ca. 40 nm thick aluminum on one (down) side of the CS film, using an Edwards evaporator at a base pressure of ca. 2·10^{-5} mbar. Pentacene-based TFTs were prepared by thermal deposition, on the other side of the CS film, a ca. 150 nm thick pentacene film. Transistors with F8T2 were prepared in air by spin coating from a xylene solution. A thin F8T2 film (ca. 100 nm thick) was thus formed on the CS film surface, opposite to the Al gate contact. These two types of device structures (with pentacene and F8T2) were then transferred inside a glove box (<0.1 ppm O_2, <0.3 ppm H_2O) to deposit source and drain contacts. P3HT was deposited by spin coating inside the glove box. The film thickness is ca. 40 nm. These three types of thin film transistors were then finalized by depositing source and drain contacts through a shadow mask, defining a channel with a length of L = 100 µm and a width of W = 5.1 mm. Source and drain contacts consisted of a bilayer structure, comprising MoO_3 (ca. 20 nm thick, which is a very efficient hole-injection material) covered with a 50–60 nm thick layer of Al. These transistors were characterized inside the glove box (to avoid damaging the source and drain contacts upon contact with ambient humidity), 24–48 h after the source and drain deposition, using a Semiconductor Parameter Analyzer HP 4140B. We note that the three types of devices were prepared from the same large CS film. We therefore expect that the characteristics of the CS dielectric, namely the water content, are similar for all three TFT structures. The thicknesses of the organic semiconductors were determined in films deposited, under similar conditions, on a glass substrate, using a Dektak 6M profilometer.

The specific capacitance of the CS film was measured with an Impedance Analyzer HP4192A at 1 kHz. A capacitor structure was fabricated by ink jet printing silver of both sides of the CS film, using a Dimatix printer.

## 3. Results and discussion

Figure 1 shows the topography image obtained over an area of 10 × 10 µm^2. Some fibers with diameters...
on the order of 150 nm are observed. The regions between the fibers appear very regular, with a root-mean-square roughness ($R_{\text{rms}}$) not exceeding 1 nm.

The specific capacitance of the CS film measured for the capacitor structure fabricated by ink jet printing silver of both sides of the CS film at 1 kHz is $C_0 = 9.54 \cdot 10^{-9}$ F/cm$^2$. We note that, a lower capacitance value of $3.26 \cdot 10^{-11}$ F/cm$^2$ was measured, also at 1 kHz, when the CS film is placed in between two gold discs. We attribute this difference to the porous nature of the CS films surface, leading to an effective thickness that is much lower than the ‘geometrical’ value considered when using the gold discs. Although Figure 1 evidences a flat surface, a detailed study over smaller areas, confirms the existence of a porous structure, as shown in Figure 2.

Though the line profile shows that such pores are few nanometers deep. However, this is likely limited by the tip radius, thus preventing us to determine the real value.

We further note that a comparable specific capacitance of ca. $10^{-7}$ F/cm$^2$ was determined at 1 kHz in a drop cast film of chitosan on ITO, though the film thickness was not reported [10]. It should be pointed out that, in view of its highly hygroscopic nature, the dielectric response of the chitosan is dependent on the water content [12] and likely on the deacetylation degree, leading to variable values in different reports.

Figure 3 shows the output characteristics and the transfer curve for the pentacene-based TFT. The curves are typical of a p-type TFT working in accumulation, as typically found in pentacene-based TFTs. The output curves were recorded at two different sweep rates. We find that a lower sweep rate leads to slightly higher currents. The transfer curve exhibits a marked hysteresis, which is attributed to mobile ions within the chitosan dielectric [10]. This hysteresis, with an increase of the current in the reverse sweep, is consistent with the output current increase upon decrease of the sweep rate (Figure 3a) and attributed to ion displacements within CS film. The transfer curve shows a low On/Off current ratio (between $V_g = -20$ and $V_g = 0$ V) of about 70 (in the forward sweep), which is reduced to ca. 4 in the back sweep. From the linear region of the ($I_d$)$^{1/2}$ vs...
Figure 3. Output and transfer curves for a pentacene TFT. a) Two measuring rates were used, 0.5 V/s (closed symbols) and 0.5 V/5 s (open symbols). The inset shows the TFT scheme. The transfer curve (b) was measured at rate of 0.5 V/s. In b) we also show the plot of ($I_d$)$^{1/2}$ as a function of the gate voltage ($V_g$) for the forward (+4 to –20 V) sweep.

Figure 4. Transfer curves of TFTs based on F8T2 (a) and P3HT (b). The arrows indicate the voltage sweep. Also shown is ($I_d$)$^{1/2}$ vs $V_g$ (open symbols).

$V_g$ plot, we estimate a saturation mobility of 0.13 cm$^2$/V·s, using the well-known relation given by Equation (1):

$$I_d = \frac{\mu W C_0}{2L} (V_g - V_{th})^2$$

These results show that CS dielectric allows the formation of a conducting channel with these three organic semiconductors. The On/Off ratios are very small, but the mobilities are close to the highest values reported for other dielectrics. For instance, a mobility of 0.4 cm$^2$/V·s was reported for pentacene TFTs with a silane-modified SiO$_2$ dielectric and MoO$_3$/Al source and drain contacts [13]. We note that, for similarly prepared TFTs with 625 nm thick SiO$_2$ dielectric, we obtained a mobility of ca. 8·10$^{-4}$ cm$^2$/V·s for P3HT, which is lower than the value obtained with CS dielectric. However, this is also much lower than the values reported for primed P3HT transistors (e.g. 0.12 cm$^2$/V·s [14]).

Finally, mobilities on the order of 10$^{-2}$ cm$^2$/V·s were reported for optimized TFTs structures based on aligned F8T2 [15]. It should be emphasized that no surface treatment of the CS film and no thermal annealing was carried out on the organic semiconductors.
ducting films. We therefore anticipate that there is still room for improvement of the performance here reported. The other significant characteristic of the CS TFTs is the presence of significant hysteresis. Figure 5 shows the effect of the hysteresis on the transfer curve of pentacene-based transistors on five sequential voltage cycles. Figure 3 showed that the Off current in the back sweep is much higher than that measured in the forward sweep. Figure 5a shows the transfer curves obtained in the forward sweep of each cycle, along 4 sequential cycles. The measured curve remains relatively constant between the 2nd and 4th cycles. If the TFT is left to recover for two hours we obtain a transfer curve very similar to the first one. The complete voltage cycles are shown in Figure 5b, where the forward sweep corresponds to the filled symbols and the back sweep to the open symbols. We attribute these results to ionic displacements within the CS dielectric in response to the applied gate bias.

It was mentioned above that CS is a hygroscopic material and that its proton conductivity has indeed been explored in previous studies. The effect of the water content was not addressed here but, based on the studies carried out by Kaihovirta et al. [16], for instance, in TFTs with hygroscopic dielectrics, the water content may in fact play an important role on the performance of transistors. Additional studies are in progress to better evaluate the role of the water content and the possible electric-double-layer effects on the behavior of the organic transistors.

4. Conclusions
In summary, we showed that self-standing chitosan films are very interesting dielectrics for organic TFTs. Transistors prepared with either thermal evaporated or spin coated films of organic semiconductors showed high hole field-effect mobilities though the On/Off ratios are small. We believe these studies open the way for the application of CS films in organic electronics for a wide range of applications, ranging from disposable systems to biosensors and biomedical applications [17].

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